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87264.2064SERIAL NO.  
09/920,751APPLICANT  
Harry V. Paul et al.FILING DATE  
08/03/01GROUP  
2184

## U.S. PATENT DOCUMENTS

	EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
A	NI	5,455,917	10/03/95	Holeman et al.	395	287	02/01/95
B	NI	5,999,527	12/07/99	Petersen	370	360	07/31/98
C	NI	5,844,887	12/01/98	Oren et al.	370	218	11/30/95
D	NI	5,983,260	11/09/99	Hauser et al.	709	201	07/18/96
E	NI	6,067,286	05/23/00	Jones et al.	370	218	04/09/96
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## FOREIGN PATENT DOCUMENTS

	EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation
U	NI	0959591	05/20/99	EP			
V	NI	1016980	12/06/99	EP			
W							
X							
Y							
Z							
AA							
BB							
CC							
DD							
EE							
FF							
GG							

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

HH	NI	Rathgeb, E.P., "Redundancy Concepts for a Large ATM Switching Node" Iss '97, Vol. 1, 21 September 1997, pgs. 425-433. —
II	NI	Fischer W., et al., "A Scalable ATM Switching System Architecture" IEEE Journal on Selected Areas in Communications, Vol. 9, 10/01/91, pgs. 1299-1307. —
JJ		
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EXAMINER

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DATE CONSIDERED

3-28-04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

